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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/382,182	08/24/1999	YOSHIO HAGIWARA	12052.20US01	4904
25550	590 02/25/2003			
MERCHANT & GOULD PC			EXAMINER	
P.O. BOX 2903 MINNEAPOLI	3 IS, MN 55402-0903		GOUDREAU	, GEORGE A
			ART UNIT	PAPER NUMBER
			1763	

DATE MAILED: 02/25/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

		49
Office Action Summary	Application No. 09-382, 182  Examiner  Gloyge	Applicant(s)  Hagiwara et, al  Group Art Unit  1762
- The MAILING DATE of this communication appears	a d	neath th correspondence address—
Period for Reply	_	
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO THIS COMMUNICATION.	O EXPIRE $3$	MONTH(S) FROM THE MAILING DATE
<ul> <li>Extensions of time may be available under the provisions of 37 CFR from the mailing date of this communication.</li> <li>If the period for reply specified above is less than thirty (30) days, a religious of the period for reply is specified above, such period shall, by defauting the reply within the set or extended period for reply will, by statement of the period by the Office later than three months after the mattern adjustment. See 37 CFR 1.704(b).</li> </ul>	reply within the statutory minin It, expire SIX (6) MONTHS from tute, cause the application to	num of thirty (30) days will be considered timely.  In the mailing date of this communication.  become ABANDONED (35 U.S.C. § 133).
Responsive to communication(s) filed on	021(ů,-	Paper # 13)-
<ul> <li>Since this application is in condition for allowance excep accordance with the practice under Ex parte Quayle, 193</li> </ul>		scution as to the merits is closed in
Disposition of Claims		
7 Claim(s) 14-10		is/are pending in the application.
Of the above claim(s)		<b>c</b> .
☐ Claim(s)		is/are allowed.
(Claim(s) 1,9-10-		is/are reje <u>cted</u> .
☐ Claim(s)		•
□ Claim(s)		are subject to restriction or election
Application Papers	•	requirement
☐ The proposed drawing correction, filed on	is 🗆 approved 🗆	disapproved.
☐ The drawing(s) filed on is/are object	cted to by the Examiner	
☐ The specification is objected to by the Examiner.		
☐ The oath or declaration is objected to by the Examiner.		
Pri rity under 35 U.S.C. § 119 (a)-(d)		
Acknowledgement is made of a claim for foreign priority	under 35 U.S.C. § 119 (a)-	(d).
¹S∕ All ☐ Some* ☐ None of the:	<u> </u>	
VI Certified copies of the priority documents have been i	eceived.	
Certified copies of the priority documents have been r		•
☐ Copies of the certified copies of the priority document		
in this national stage application from the International		<b>))</b>
*Certified copies not received:	· · · · · · · · · · · · · · · · · · ·	
Attachment(s)		
☐ Information Disclosure Statem nt(s), PTO-1449, Paper No.	o(s) 🗆 Int	rvi w Summary, PTO-413

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

□ Notice of Reference(s) Cited, PTO-892

Office Action Summary

☐ Notice of Informal Pat nt Application, PTO-152

□ Oth r\_\_\_\_\_

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- 15. This action will not be made final due to the new grounds of rejection.
- 16. Claims 1, 4-10 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

-Claims 1, and 9 recite a process for forming a via hole in which the via hole is filled with a conductive material prior to removing (i.e.- ashing) the photo resist etch mask which was used in the patterning of the via hole. This process sequence conflicts with the process sequence recited on page 15 of applicant's specification which recite that the photo resist etch mask is removed (i.e.-ashed) prior to filling the via hole with a conductive material. On the other hand, page 5 of applicant's specification recites the same sequence of process steps as that which is claimed by the applicant. The examiner believes, however, based upon his knowledge of semiconductor processing methods that the sequence of process steps recited by applicant on page 5 is artifact created by the translator. The examiner further believes based upon this knowledge of semiconductor processing methods that the correct sequence of process steps is recited on page 15 of applicant's specification.

17. Since the examiner is unable to ascertain what sequence of process steps applicant is trying to claim in their claims, the examiner has not rejected any of applicant's claims over prior art.

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18. Any inquiry concerning this communication or earlier communications from the examiner

should be directed to Examiner George A. Goudreau whose telephone number is (703) -308-

1915. The examiner can normally be reached on Monday through Friday from 9:30 to 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor,

Examiner Gregory Mills, can be reached on (703) -308-1633. The appropriate fax phone number

for the organization where this application or proceeding is assigned is (703) -306-3186.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is (703) -308-0661.

Geørge A. Goudreau/gag

Primary Examiner

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